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1 Specification, validation, and synthesis of email agent controllers: A case study in function rich reactive system design
Robert J. Hall
August 2000 **Proceedings of the third workshop on Formal methods in software practice**
Full text available: pdf(527.90 KB)   Additional Information: full citation, abstract, references, index terms

With a few exceptions, previous formal methods for reactive system design have focused on finite state machines represented in terms of boolean states and boolean next-state functions. By contrast, in many reactive system domains requirements engineers and developers think in terms of complex data types and expressive next-state functions. Formal methods for reactive system design must be extended to meet their needs as well. I term a reactive system function rich if expr ...

Keywords: Electronic Mail, Formal Methods, Reactive Systems

2 Highly available systems for database applications
Won Kim
March 1984 **ACM Computing Surveys (CSUR)**, Volume 16 Issue 1
Full text available: pdf(2.43 MB)   Additional Information: full citation, abstract, references, citings, index terms, review

As users entrust more and more of their applications to computer systems, the need for systems that are continuously operational (24 hours per day) has become even greater. This paper presents a survey and analysis of representative architectures and techniques that have been developed for constructing highly available systems for database applications. It then proposes a design of a distributed software subsystem that can serve as a unified framework for constructing database applica ...

3 The local disk controller
Gilbert E. Houtekamer
August 1985 **ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 1985 ACM SIGMETRICS conference on Measurement and modeling of computer systems**, Volume 13 Issue 2
Full text available: pdf(1.02 MB)   Additional Information: full citation, abstract, references, citings, index terms

The performance of the I/O subsystem in the 370-XA architecture has been improved considerably with the introduction of the new channel subsystem, as compared to the System/370 architecture. The emphasis in the 370-XA architecture is on reducing the CPU load associated with I/O, and on reducing the congestion in multi-CPU, shared systems, by redesigning the channel system. In this paper we will show that a reallocation of the control unit logic may triple the channel subsystem's ...

4 Qn-line communications and the computer
F. K. Morioka, R. M. Wainwright
October 1969 **Proceedings of the first ACM symposium on Problems in the optimization of data communications systems**

Full text available: pdf(2.04 MB)   Additional Information: full citation, abstract, index terms

This paper contains information on the general characteristics of the Control Data Corporation, MDM Communications Division M1000 Communications Message Switching System. It is the intent of this paper to discuss the analytic systems approach to the problem that led to the building of unique hardware and software to resolve the message handling functions. The M1000 hardware and software system features are discussed in the paper.

5 The family of concurrent logic programming languages
Ehud Shapiro
September 1989 **ACM Computing Surveys (CSUR)**, Volume 21 Issue 3

Full text available: pdf(9.62 MB)   Additional Information: full citation, abstract, references, citings, index terms

Concurrent logic languages are high-level programming languages for parallel and distributed systems that offer a wide range of both known and novel concurrent programming techniques. Being logic programming languages, they preserve many advantages of the abstract logic programming model, including the logical reading of programs and computations, the convenience of representing data structures with logical terms and manipulating them using unification, and the amenability to metaprogrammin ...

6 Disk cache—miss ratio analysis and design considerations
Alan J. Smith
August 1985 **ACM Transactions on Computer Systems (TOCS)**, Volume 3 Issue 3

Full text available: pdf(3.13 MB)   Additional Information: full citation, abstract, references, citings, index terms, review

The current trend of computer system technology is toward CPUs with rapidly increasing processing power and toward disk drives of rapidly increasing density, but with disk performance increasing very slowly if at all. The implication of these trends is that at some point the processing power of computer systems will be limited by the throughput of the input/output (I/O) system. A solution to this problem, which is described and evaluated in this paper, is disk cache

7 Special issue: AI in engineering
D. Sriram, R. Joobbani
January 1985 **ACM SIGART Bulletin**, Issue 91

Full text available: pdf(8.79 MB)   Additional Information: full citation, abstract

The papers in this special issue were compiled from responses to the announcement in the July 1984 issue of the SIGART newsletter and notices posted over the ARPAnet. The interest being shown in this area is reflected in the sixty papers received from over six countries. About half the papers were received over the computer network.

8 Connection architecture and protocols to support efficient handoffs over an ATM/B-

ISDN personal communications network
Oliver T. W. Yu, Victor C. M. Leung
December 1996 **Mobile Networks and Applications**, Volume 1 Issue 4

Full text available: pdf(569.77 KB)   Additional Information: full citation, abstract, references, index terms

The next generation personal communication network will likely internetwork wireless networks via the ATM/B-ISDN to enable ubiquitous broadband personal communication services. Support of user terminal mobility, particularly the capability for fast and seamless handoffs, over the ATM/B-ISDN is an expected requirement that is not currently met. We propose extensions to the ATM/B-ISDN user transport and signaling network architectures and signaling protocols to meet these requirements. The ne ...

9 Crossover switch discovery for wireless ATM LANs
Chai-Keong Toh
October 1996 **Mobile Networks and Applications**, Volume 1 Issue 2

Full text available: pdf(569.77 KB)   Additional Information: full citation, abstract, references, citings, index terms

The emergence of Wireless Local Area Networks (WLANs) has brought about the possibility of mobile computing. In order to maintain connectivity to Mobile Hosts (MHs), a handover mechanism is needed as MHs migrate from one Base Station's (BS) wireless cell to another. Current handover schemes are mainly catered for connectionless WLANs (example Mobile IP) which do not have the ability to support Quality of Service (QoS) for continuous media traffic. Hence, mobility for connection-oriented WLA ...

10 Fast detection of communication patterns in distributed executions
Thomas Kunz, Michiel F. H. Seuren
November 1997 **Proceedings of the 1997 conference of the Centre for Advanced Studies on Collaborative research**

Full text available: pdf(4.21 MB)   Additional Information: full citation, abstract, references, index terms

Understanding distributed applications is a tedious and difficult task. Visualizations based on process-time diagrams are often used to obtain a better understanding of the execution of the application. The visualization tool we use is Poet, an event tracer developed at the University of Waterloo. However, these diagrams are often very complex and do not provide the user with the desired overview of the application. In our experience, such tools display repeated occurrences of non-trivial commun ...

11 Local networks
William Stallings
March 1984 **ACM Computing Surveys (CSUR)**, Volume 16 Issue 1

Full text available: pdf(3.01 MB)   Additional Information: full citation, abstract, references, citings, index terms, review

The rapidly evolving field of local network technology has produced a steady stream of local network products in recent years. The IEEE 802 standards that are now taking shape, because of their complexity, do little to narrow the range of alternative technical approaches and at the same time encourage more vendors into the field. The purpose of this paper is to present a systematic, organized overview of the alternative architectures for and design approaches to local networks.

...

12 The HP AutoRAID hierarchical storage system
John Wilkes, Richard Golding, Carl Staelin, Tim Sullivan
February 1996 **ACM Transactions on Computer Systems (TOCS)**, Volume 14 Issue 1

Full text available: pdf(1.82 MB)   Additional Information: full citation, abstract, references, citings, index terms

Configuring redundant disk arrays is a black art. To configure an array properly, a system administrator must understand the details of both the array and the workload it will support. Incorrect understanding of either, or changes in the workload over time, can lead to poor performance. We present a solution to this problem: a two-level storage hierarchy implemented inside a single disk-array controller. In the upper level of this hierarchy, two copies of active data are stored to provide f ...

Keywords: RAID, disk array, storage hierarchy

13 The space shuttle primary computer system
Alfred Spector, David Gifford
September 1984 **Communications of the ACM**, Volume 27 Issue 9
Full text available: pdf(5.34 MB)   Additional Information: full citation, references, citings, index terms

Keywords: PASS, avionics system, space shuttle

14 Special section: Reasoning about structure, behavior and function
B. Chandrasekaran, Rob Milne
July 1985 **ACM SIGART Bulletin**, Issue 93
Full text available: pdf(5.13 MB)   Additional Information: full citation, abstract, references

The last several years' of work in the area of knowledge-based systems has resulted in a deeper understanding of the potentials of the current generation of ideas, but more importantly, also about their limitations and the need for research both in a broader framework as well as in new directions. The following ideas seem to us to be worthy of note in this connection.

15 Neon: a single-chip 3D workstation graphics accelerator
Joel McCormack, Robert McNamara, Christopher Gianos, Larry Seiler, Norman P. Jouppi, Ken Correll
August 1998 **Proceedings of the ACM SIGGRAPH/EUROGRAPHICS workshop on Graphics hardware**
Full text available: pdf(1.58 MB)   Additional Information: full citation, references, citings, index terms

Keywords: chunk rendering, direct rendering, graphics pipeline, level of detail, rasterization, texture cache, tile rendering

16 Encryption and Secure Computer Networks
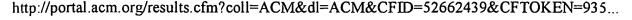Gerald J. Popek, Charles S. Kline
December 1979 **ACM Computing Surveys (CSUR)**, Volume 11 Issue 4
Full text available: pdf(2.50 MB)   Additional Information: full citation, references, citings, index terms

17 Writing an OLE automation controller in Ada95
Chad Bremmon

May 1997 **ACM SIGAda Ada Letters**, Volume XVII Issue 3

Full text available: 🔳 pdf(660.52 KB)   Additional Information: full citation, abstract, index terms

Microsoft OLE provides a powerful toolset to the Microsoft WindowsNT/95 developer. We have experienced the development of an OLE Automation Controller in Ada95 and would like to discuss the experience. This paper details the steps necessary to develop an OLE Automation Controller in Ada95.

18 The HP AutoRAID hierarchical storage system

J. Wilkes, R. Golding, C. Staelin, T. Sullivan
December 1995 **ACM SIGOPS Operating Systems Review , Proceedings of the fifteenth ACM symposium on Operating systems principles**, Volume 29 Issue 5

Full text available: 🔳 pdf(1.60 MB)   Additional Information: full citation, references, citings, index terms

19 A passive protected self-healing mesh network architecture and applications

Tsong-Ho Wu
February 1994 **IEEE/ACM Transactions on Networking (TON)**, Volume 2 Issue 1

Full text available: 🔳 pdf(1.67 MB)   Additional Information: full citation, references, citings, index terms, review

20 The m-calculus: a higher-order distributed process calculus

Alan Schmitt, Jean-Bernard Stefani
January 2003 **ACM SIGPLAN Notices , Proceedings of the 30th ACM SIGPLAN-SIGACT symposium on Principles of programming languages**, Volume 38 Issue 1

Full text available: 🔳 pdf(301.25 KB)   Additional Information: full citation, abstract, references, citings, index terms

This paper presents a new distributed process calculus, called the M-calculus, that can be understood as a higher-order version of the Distributed Join calculus with programmable localities. The calculus retains the implementable character of the Distributed Join calculus while overcoming several important limitations: insufficient control over communication and mobility, absence of dynamic binding, and limited locality semantics. The calculus is equipped with a polymorphic type system that guar ...

Keywords: distribution, higher order, mobility, process calculi, type systems

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